

PATENT NUMBER

U.S. UTILITY Patent Application

12W SCANNED	O.I.P.E. BXD Q.A. MC	PATENT-DATE
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APPLICATION NO.	CONT/PRIOR	CLASS	*SUBCLASS	ART UNIT	EXAMINER
09/517987	D	437 438	320	1756	Booth

APPLICANTS

Kuei-Wu Huang
Tsiu Chan
Gregory Smith

TITLE

Method of forming planarized structures in an integrated circuit

PTO-2040
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[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS Sheets Dwg. Figs. Dwg. Print Fig.		CLAIMS ALLOWED Total Claims Paid Claims	
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